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	WESTERMAN, HATTORI, DANIELS & ADRIA 1250 CONNECTICUT AVENUE, NW SUITE 700 WASHINGTON, DC 20036									
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03 F	C:8001 APPLICATION NO.	9.00 BP FILING DATE	PRAUENARY	FIRST NAME	ED INVENTO	DR.	ATTORNEY	DOCKET NO.	CONFIRMATION NO.	
	10/098,535 03/18/2002				Mizumasa		020348		6232	
	TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING A PLURALITY OF CIRCUIT REGIONS WHERE DIFFERENT POWER SUPP VOLTAGES ARE USED AND METHOD OF MANUFACTURING THE SAME									
	APPLN, TYPE	SMALL ENTITY ISSUE FEE		EE	PUBI	LICATION FEE	TOTAL F	EE(S) DUE	DATE DUE	
	nonprovisional NO		\$1330			\$300	\$1630		12/28/2004	
	EXAMINER		ART UNIT		CLAS	SS-SUBCLASS]			
4	WHITMORE, STACY		2812		7	16-005000	_			
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	(A) NAME OF ASSIGNI FUJITSI	(E	(B) RESIDENCE: (CITY and STATE OR COUNTRY) Kawasaki, Japan							
	Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Governm 4a. The following fec(s) are enclosed: 4b. Payment of Fec(s):									
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	Authorized Signature					Date/	2/14/	04		
	Typed or printed name Thomas E. Brown				_	Registration	No4	4,450		
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